

Serial No.09/942,328
HP Docket No: 10008019-1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Venkitakrishnan et al.

Serial No.:09/942,328

Group Art Unit: 2186

Filed: August 28, 2001

Examiner: Kim, Hong Chong

For: A STREAMLINED CACHE COHERENCY PROTOCOL SYSTEM
AND METHOD FOR A MULTIPLE PROCESSOR SINGLE CHIP
DEVICE

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Honorable Commissioner of Patents and Trademarks
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

Sir:

In response to the Office Action dated January 10, 2005, please enter the
following amendments and remarks into the above-identified application:

Amendments begin on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.

Please
enter
4/25/05
HCC